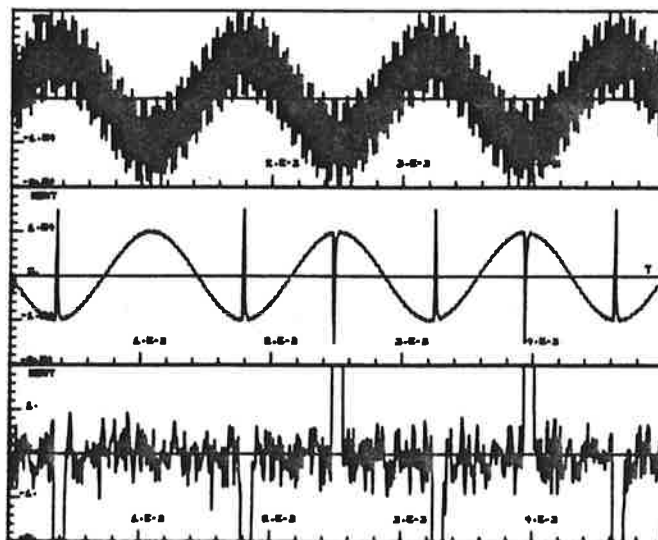


CAD for Digital Signal Processing

*September 9 - 12, 1986
Leuven - Belgium*



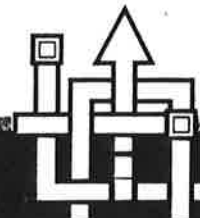
*CAD tools for Wave Digital Filter Design
and general DSP-algorithm analysis and
simulation developed in ESPRIT-97.*

*Workshop sponsored by the
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<u>Tuesday, Sept. 9, '86</u>		<u>Wednesday, Sept. 10, '86</u>		<u>Thursday, Sept. 11, '86</u>	<u>Friday, Sept. 12, '86</u>
8.30h	Registration				
9.00h	Registration	9.00h	Claesen : Analysis & simulation of digital filters.	Van Meerbergen : Signal processors.	Gazsi : Design of Multi-rate WDF-filter.
9.30h	Van Overstraeten : Introduction.				
10.00h	Fettweis : Wave digital filters introduction.	10.15h	Coffee	Coffee	Coffee
		10.30h	Jain : Optimization of CSD coefficients.	Van Ginderdeuren : Bit-serial VLSI-DSP implementation.	Gazsi : Practical semi-Multi rate synthesis + lab-testing.
11.00h	Coffee				
11.30h	Gazsi : Computer-aided synthesis of WDF.	11.45h	Catthoor : Finite word-length effects.	De Man : CATHEDRAL silicon compiler.	Practical session : participants projects.
13.00h	Lunch	13.00h	Lunch	Lunch	Lunch
14.00h	Gazsi : introduction and practical session on FALCON.	14.00h	Catthoor, Claesen : Introduction and practical session on DIGEST.	Demo of CATHEDRAL-1 silicon compiler. Jain : practical session CSD optimization.	Practical session : own projects (continued).
16.00h	Coffee	16.00h	Coffee	Coffee	
16.15h	Practical session on FALCON (contin.).	16.15h	Practical session on DIGEST finite word-length effects.	Jain (continued) Catthoor (CSD optimisation).	
18.30h			Guided visit to the Bequinage.		
19.30h			Course banquet.		

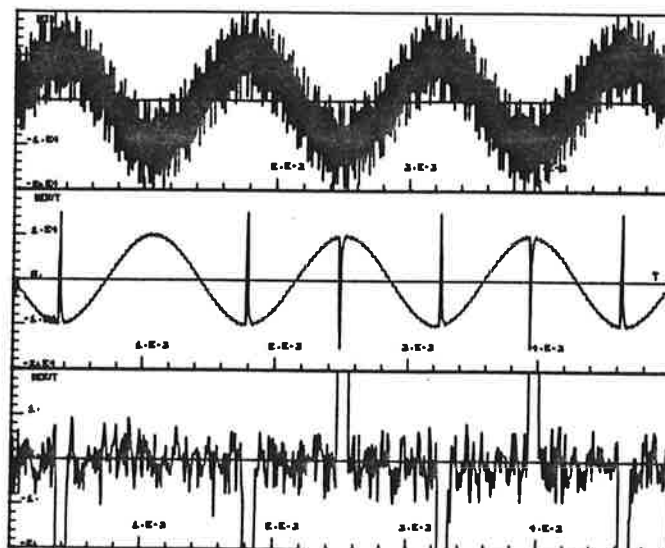
Workshop
CAD for Digital Signal Processing
Contents

- A. Fettweis, *Wave Digital filters : Theory and Practice.*
 - L. Gazsi, *Computer-Aided Synthesis of Wave Digital Filters.*
 - L. Claesen, *Analysis and simulation of digital filters.*
 - R. Jain, *Optimization of Canonical Signed Digit Coefficients.*
 - F. Catthoor, *Characterization of Finite Word Length Effects and Optimization of Coefficients by Simulated Annealing.*
 - J. Van Meerbergen, *Architectures and Characteristics of Commercially Available General Purpose Signal Processors.*
 - J. Van Ginderdeuren, *Custom bit-serial VLSI implementations of DSP algorithms.*
 - H. De Man, *The CATHEDRAL silicon compiler.*
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